

# EAST SEARCH TLM 08/03/2007

	Type	Ref #	Hits	Search Text	DBs
1	BRS	S1	1023	processors (identification or ID) memory stor\$5 control stall (data adj path\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB
2	BRS	S2	180	processors (identification or ID) memory stor\$5 control stall (data adj path\$2) (circuit adj board)	US-PGPUB; USPAT; USOCR; IBM_TDB
3	BRS	S3	111	processors (identification or ID) memory stor\$5 control stall (data adj path\$2) (circuit adj board) (data adj flow)	US-PGPUB; USPAT; USOCR; IBM_TDB
4	BRS	S4	0	processors (identification or ID) memory stor\$5 control stall (data adj path\$2) (circuit adj board) (data adj flow) (setting adj data)	US-PGPUB; USPAT; USOCR; IBM_TDB
5	BRS	S7	3070	(semiconductor adj substrate) (circuit adj board)	USPAT
6	BRS	S6	5186	(semiconductor adj substrate) (circuit adj board)	US-PGPUB; USPAT; USOCR; IBM_TDB
7	BRS	S5	0	(semiconductor adj substrate) (circuit adj board)	US-PGPUB; USPAT; USOCR; IBM_TDB
8	BRS	S8	834	(DOERR.in. and MICHAEL.in.) or (HALLIDY.in. and WILLIAM.in.) or (GIBSON.in. and DAVID.in.) or (CHASE.in. and "CRAIG.in")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
9	BRS	S9	838	(DOERR.in. and MICHAEL.in.) or (HALLIDY.in. and WILLIAM.in.) or (GIBSON.in. and DAVID.in.) or (CHASE.in. and CRAIG.in.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

	Type	Ref #	Hits	Search Text	DBs
10	BRS	S10	128	((DOERR.in. and MICHAEL.in.) or (HALLIDY.in. and WILLIAM.in.) or (GIBSON.in. and DAVID.in.) or (CHASE.in. and CRAIG.in.)) and processor\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
11	BRS	S11	88	((DOERR.in. and MICHAEL.in.) or (HALLIDY.in. and WILLIAM.in.) or (GIBSON.in. and DAVID.in.) or (CHASE.in. and CRAIG.in.)) and processor\$3 and configur\$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
12	BRS	S12	2	((DOERR.in. and MICHAEL.in.) or (HALLIDY.in. and WILLIAM.in.) or (GIBSON.in. and DAVID.in.) or (CHASE.in. and CRAIG.in.)) and processor\$3 and configur\$5 and stall\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
13	BRS	S13	5005	processor\$3 and configur\$5 and stall\$3	US-PGPUB
14	BRS	S16	448	processor\$3 and configur\$5 and stall\$3 and routing and engine and source and intermediate	US-PGPUB
15	BRS	S14	747	processor\$3 and configur\$5 and stall\$3 and routing and engine	US-PGPUB
16	BRS	S17	285	processor\$3 and configur\$5 and stall\$3 and routing and engine and source and intermediate and buffered	US-PGPUB
17	BRS	S15	667	processor\$3 and configur\$5 and stall\$3 and routing and engine and source	US-PGPUB
18	BRS	S18	271	processor\$3 and configur\$5 and stall\$3 and routing and engine and source and intermediate and buffered and communication	US-PGPUB
19	BRS	S20	161	processor\$3 and dynamically and configur\$5 and stall\$3 and routing and engine and source and intermediate and buffered and communication and port	US-PGPUB

	Type	Ref #	Hits	Search Text	DBs
20	BRS	S19	164	processor\$3 and dynamically and configur\$5 and stall\$3 and routing and engine and source and intermediate and buffered and communication	US-PGPUB
21	BRS	S22	1	processor\$3.clm. and dynamically.clm. and configur\$5.clm. and stall\$3.clm.and source.clm. and intermediate.clm. and buffered.clm.	US-PGPUB
22	BRS	S21	0	processor\$3.clm. and dynamically.clm. and configur\$5.clm. and stall\$3.clm. and routing.clm. and engine.clm. and source.clm. and intermediate.clm. and buffered.clm. and communication.clm. and port.clm.	US-PGPUB
23	BRS	S24	511	712/14-16.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
24	BRS	S25	89	712/17.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
25	BRS	S27	668	"712/225".ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

	Type	Ref #	Hits	Search Text	DBs
26	BRS	S28	2097	710/52.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
27	BRS	S29	399	710/53.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
28	BRS	S30	276	710/54.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
29	BRS	S31	524	710/56.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
30	BRS	S32	477	710/58.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
31	BRS	S33	119	710/59.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

	Type	Ref #	Hits	Search Text	DBs
32	BRS	S34	351	712/219.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
33	BRS	S23	651	712/10-12.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
34	BRS	S37	26625	stall	US-PGPUB; USPAT
35	BRS	S36	661	712/225.ccls.	US-PGPUB; USPAT
36	BRS	S44	8	transmit\$4 near2 stall\$3 near2 data and processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
37	BRS	S42	24	transmit\$4 near2 stall\$3 near2 information	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
38	BRS	S61	448	processor\$3 and configur\$5 and stall\$3 and routing and engine and source and intermediate	US-PGPUB
39	BRS	S55	834	(DOERR.in. and MICHAEL.in.) or (HALLIDY.in. and WILLIAM.in.) or (GIBSON.in. and DAVID.in.) or (CHASE.in. and "CRAIG.in")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

	Type	Ref #	Hits	Search Text	DBs
40	BRS	S39	15	stall\$3 with ((suspend\$3 or halt\$3 or paus\$3) near2 transfer\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
41	BRS	S45	217	(transmit\$4 or send\$3 or propagat\$3 or transfer\$4) near2 stall\$3 near2 (data or information) and processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
42	BRS	S46	69	(transmit\$4 or send\$3 or propagat\$3 or transfer\$4) near2 (stall\$3 adj (data or information)) and processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
43	BRS	S43	19	transmit\$4 near2 stall\$3 near2 information and processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
44	BRS	S48	1235	processors (identification or ID) memory stor\$5 control stall (data adj path\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB
45	BRS	S47	12	(transmit\$4 or send\$3 or propagat\$3 or transfer\$4) near2 (stall\$3 adj (data or information)) and processor and intermediate	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
46	BRS	S50	145	processors (identification or ID) memory stor\$5 control stall (data adj path\$2) (circuit adj board) (data adj flow)	US-PGPUB; USPAT; USOCR; IBM_TDB

	Type	Ref #	Hits	Search Text	DBs
47	BRS	S51	0	processors (identification or ID) memory stor\$5 control stall (data adj path\$2) (circuit adj board) (data adj flow) (setting adj data)	US-PGPUB; USPAT; USOCR; IBM_TDB
48	BRS	S35	239	712/15.ccls.	US-PGPUB; USPAT
49	BRS	S54	1	(semiconductor adj substrate) (circuit adj board)	US-PGPUB; USPAT; USOCR; IBM_TDB
50	BRS	S62	747	processor\$3 and configur\$5 and stall\$3 and routing and engine	US-PGPUB
51	BRS	S64	667	processor\$3 and configur\$5 and stall\$3 and routing and engine and source	US-PGPUB
52	BRS	S66	161	processor\$3 and dynamically and configur\$5 and stall\$3 and routing and engine and source and intermediate and buffered and communication and port	US-PGPUB
53	BRS	S56	838	(DOERR.in. and MICHAEL.in.) or (HALLIDY.in. and WILLIAM.in.) or (GIBSON.in. and DAVID.in.) or (CHASE.in. and CRAIG.in.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
54	BRS	S57	128	((DOERR.in. and MICHAEL.in.) or (HALLIDY.in. and WILLIAM.in.) or (GIBSON.in. and DAVID.in.) or (CHASE.in. and CRAIG.in.)) and processor\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
55	BRS	S58	88	((DOERR.in. and MICHAEL.in.) or (HALLIDY.in. and WILLIAM.in.) or (GIBSON.in. and DAVID.in.) or (CHASE.in. and CRAIG.in.)) and processor\$3 and configur\$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
56	BRS	S60	5005	processor\$3 and configur\$5 and stall\$3	US-PGPUB

	Type	Ref #	Hits	Search Text	DBs
57	BRS	S65	271	processor\$3 and configur\$5 and stall\$3 and routing and engine and source and intermediate and buffered and communication	US-PGPUB
58	BRS	S63	285	processor\$3 and configur\$5 and stall\$3 and routing and engine and source and intermediate and buffered	US-PGPUB
59	BRS	S69	0	processor\$3.clm. and dynamically.clm. and configur\$5.clm. and stall\$3.clm. and routing.clm. and engine.clm. and source.clm. and intermediate.clm. and buffered.clm. and communication.clm. and port.clm.	US-PGPUB
60	BRS	S71	89	712/17.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
61	BRS	S72	668	"712/225".ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
62	BRS	S80	651	712/10-12.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
63	BRS	S68	1	processor\$3.clm. and dynamically.clm. and configur\$5.clm. and stall\$3.clm. and source.clm. and intermediate.clm. and buffered.clm.	US-PGPUB



	Type	Ref #	Hits	Search Text	DBs
64	BRS	S59	2	((DOERR.in. and MICHAEL.in.) or (HALLIDY.in. and WILLIAM.in.) or (GIBSON.in. and DAVID.in.) or (CHASE.in. and CRAIG.in.)) and processor\$3 and configur\$5 and stall\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
65	BRS	S49	216	processors (identification or ID) memory stor\$5 control stall (data adj path\$2) (circuit adj board)	US-PGPUB; USPAT; USOCR; IBM_TDB
66	BRS	S67	164	processor\$3 and dynamically and configur\$5 and stall\$3 and routing and engine and source and intermediate and buffered and communication	US-PGPUB
67	BRS	S70	511	712/14-16.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
68	BRS	S73	2097	710/52.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
69	BRS	S74	399	710/53.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
70	BRS	S75	276	710/54.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

	Type	Ref #	Hits	Search Text	DBs
71	BRS	S76	524	710/56.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
72	BRS	S77	477	710/58.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
73	BRS	S78	119	710/59.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
74	BRS	S79	351	712/219.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
75	BRS	S82	333	712/22.ccls.	US-PGPUB; USPAT; USOCR; IBM_TDB
76	BRS	S52	3769	(semiconductor adj substrate) (circuit adj board)	USPAT
77	BRS	S41	2	propagat\$3 near2 stall\$3 near2 information	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

	Type	Ref #	Hits	Search Text	DBs
78	BRS	S40	1	propagat\$3 near2 stall near2 information	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
79	BRS	S38	15	stall\$3 with ((suspend\$3 or halt\$3 or paus\$3) near2 transfer\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
80	BRS	S53	7018	(semiconductor adj substrate) (circuit adj board)	US-PGPUB; USPAT; USOCR; IBM_TDB
81	BRS	S81	483	712/1.ccls.	US-PGPUB; USPAT; USOCR; IBM_TDB